

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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| Applicant(s): | Richard S. Wise (et al.) | Examiner: | Julio Maldonado |
| Serial No.: | 10/709,204 | Group Art Unit: | 2823 |
| Filed: | April 21, 2004 | Attorney Docket No: | FIS920030028US1 |
| Title: | WIRING STRUCTURE FOR INTEGRATED CIRCUIT WITH REDUCED INTRALEVEL CAPACITANCE | | |

RESPONSE

Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This paper is submitted in response to a Final Office Action mailed October 16, 2006 by the United States Patent and Trademark Office in connection with the above-identified Application. A response to this October 16, 2006 Final Office Action is due by January 16, 2007. Therefore, this paper is being timely filed.

List of Claims begin on page 2 of this paper.

Remarks begin on page 3 of this paper.